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HIT 2 482-06

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: T. WATANABE et al
Serial No. 09/198,658
Filed: November 24, 1998

jc945 U.S. PTO
09/739758
12/20/00

Group Art Unit: 2783
Examiner: E. Coleman
For: NEURAL NETWORK PROCESSING SYSTEM
USING SEMICONDUCTOR MEMORIES

INFORMATION DISCLOSURE STATEMENT

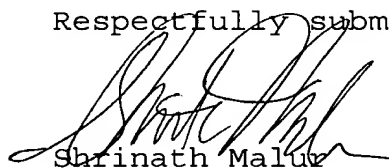
Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure, Applicants inform the Examiner of the documents cited during prosecution of the parent application, Serial No. 09/198,658.

The applicants request the Examiner to initial and return a copy of the attached PTO-1449 form to indicate that the references have been considered.

Respectfully submitted,



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Date: December 20, 2000